IN THE SPECIFICATION

Please amend the paragraph at page 20, line 19 to page 21, line 3, as follows:

FIG. 8 depicts more details of signal select circuitry 202. Persons of ordinary skill in the art who have the benefit of the description of the invention understand that signal select circuitry 202 operates similarly to signal select circuit 20~~2~~6, except that it selects multi-bit signals. More specifically, signal select circuit 202 includes a plurality of AND gates 270A-270M and a corresponding plurality of OR gates 273A-273M, where *M* denotes the number of memory address bits.

P.Le
OK
9/15/04

Please amend the paragraph at page 21, lines 11-16, as follows:

Depending on the arrangement of the memories used, ~~some of the address~~ one may use some of the address bits for bank selection, as desired. More specifically, if one uses memories arranged in banks, one may use some of the address bits to select a particular bank, as persons skilled in the art with the benefit of the description of the invention understand. For example, one may use signal 116B2A and signal 116B2B to select one of four banks.